

FIG. 3

(PRIOR ART)

LOCAL SWITCH MEMORY MAP

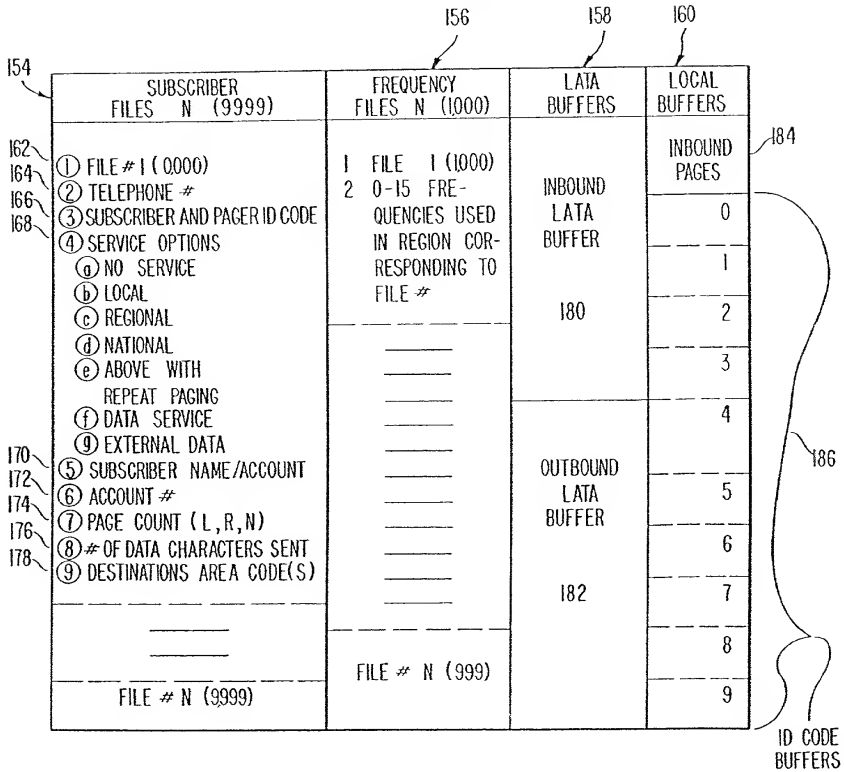


FIG. 4

(PRIOR ART)

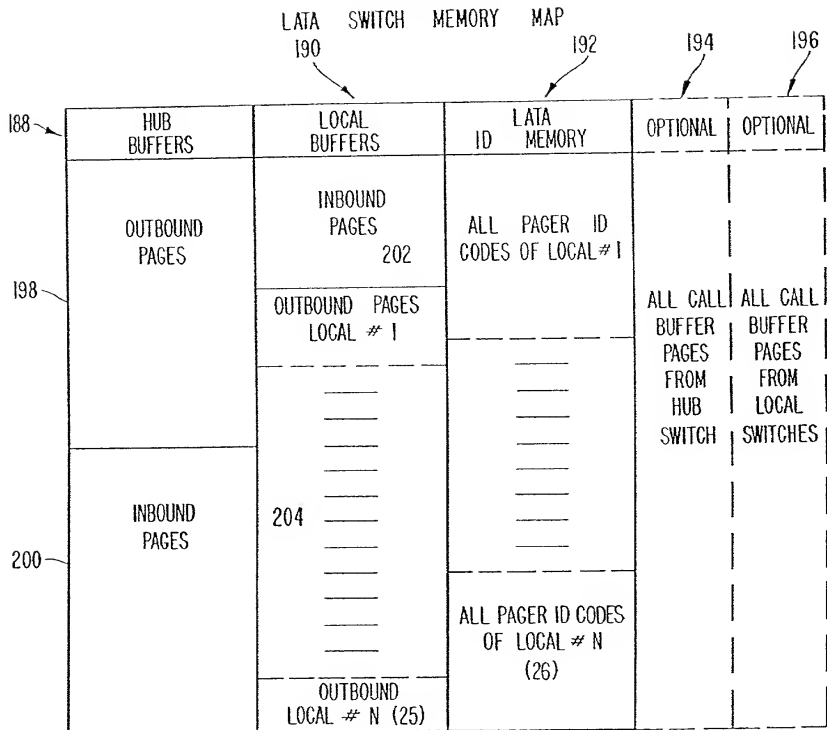


FIG. 5

(PRIOR ART)

HUB SWITCH MEMORY MAP

206 HUB BUFFERS		208 LATA BUFFERS		210 LATA CODE TABLES N (100)		212 HUB ROUTING CODES N (1000)	
INBOUND HUB # 1		INBOUND LATA # 1		LATA CODE 222 # 1		ROUTING CODE 1,2,3,4,5,6 (312)	
_____ _____ _____ _____		218 _____ _____ _____ _____		_____ ____			

214

216

FIG. 6
PRIOR ART

THE FIVE LAYER MODIFIED X.25 PACKET

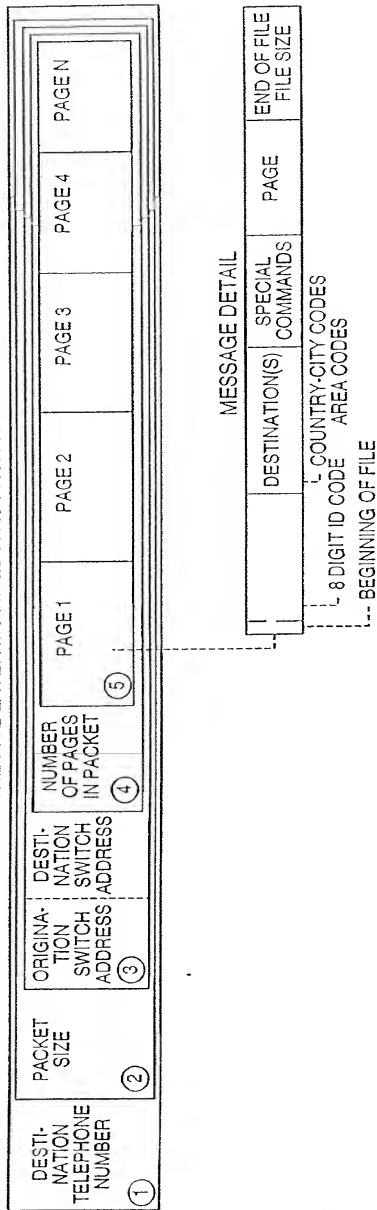


FIG. 7
PRIOR ART

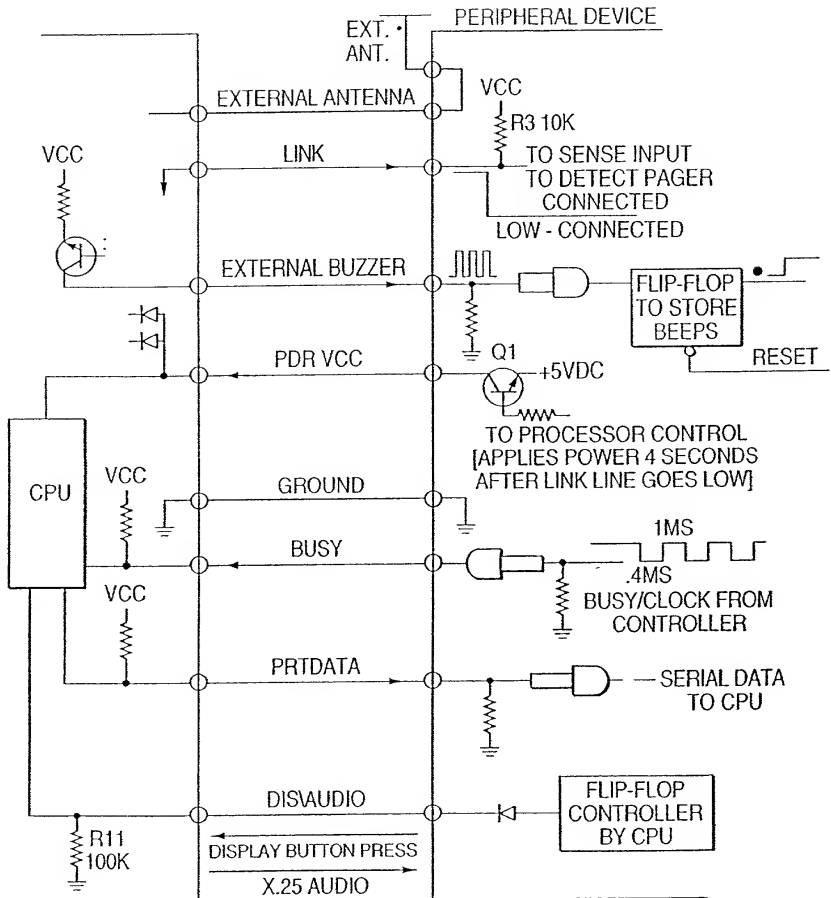


FIG. 8

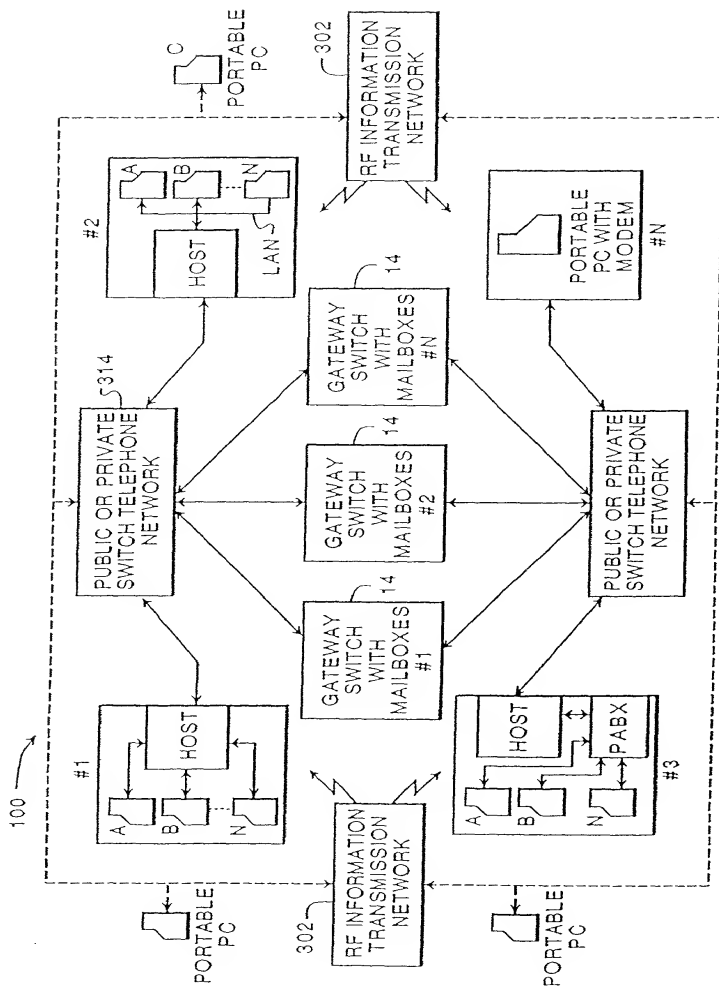


FIG. 9

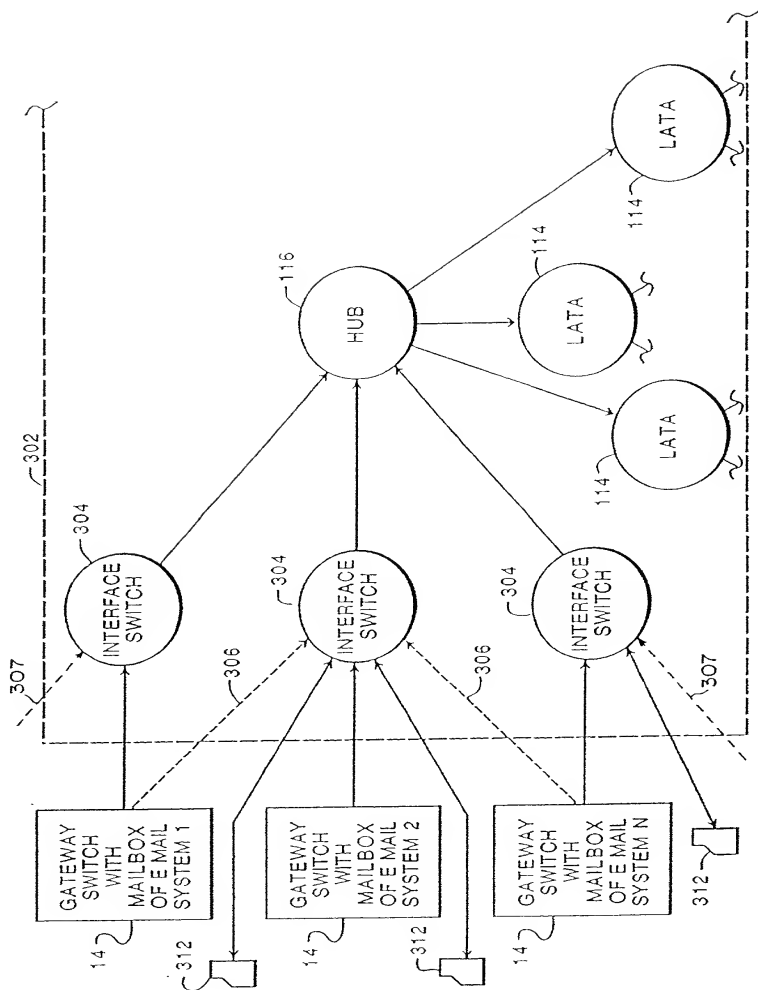
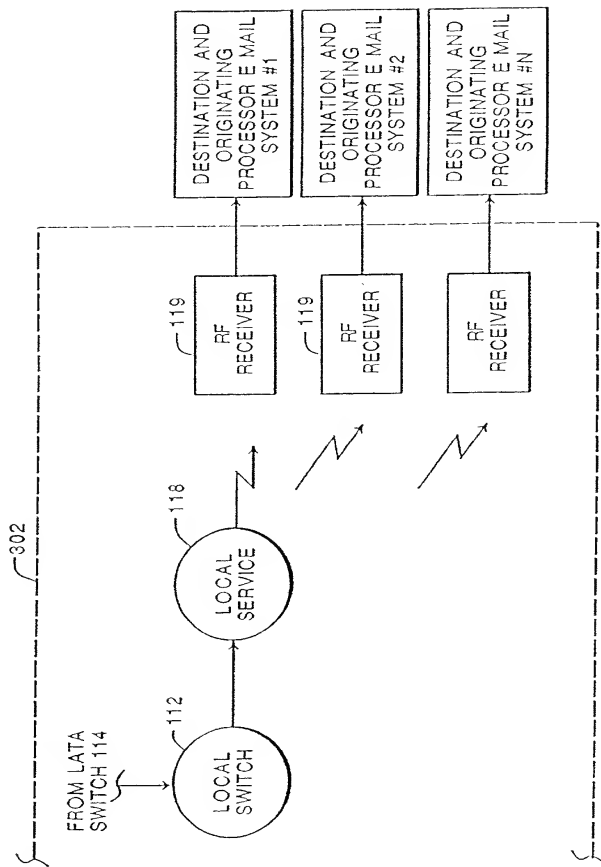


FIG. 10



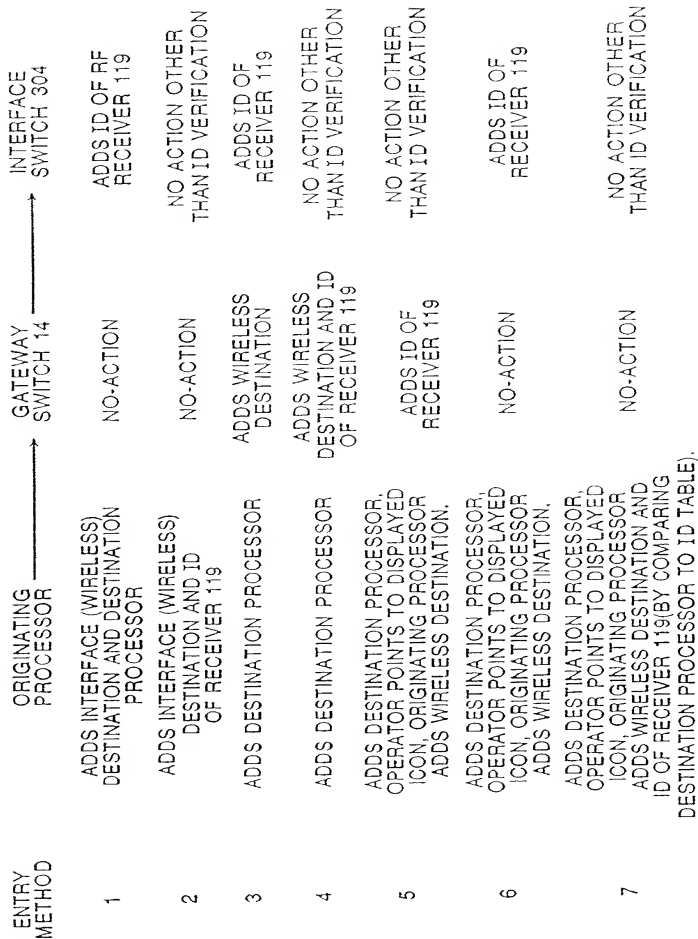


FIG. 11

